

FIG. 1 communication system

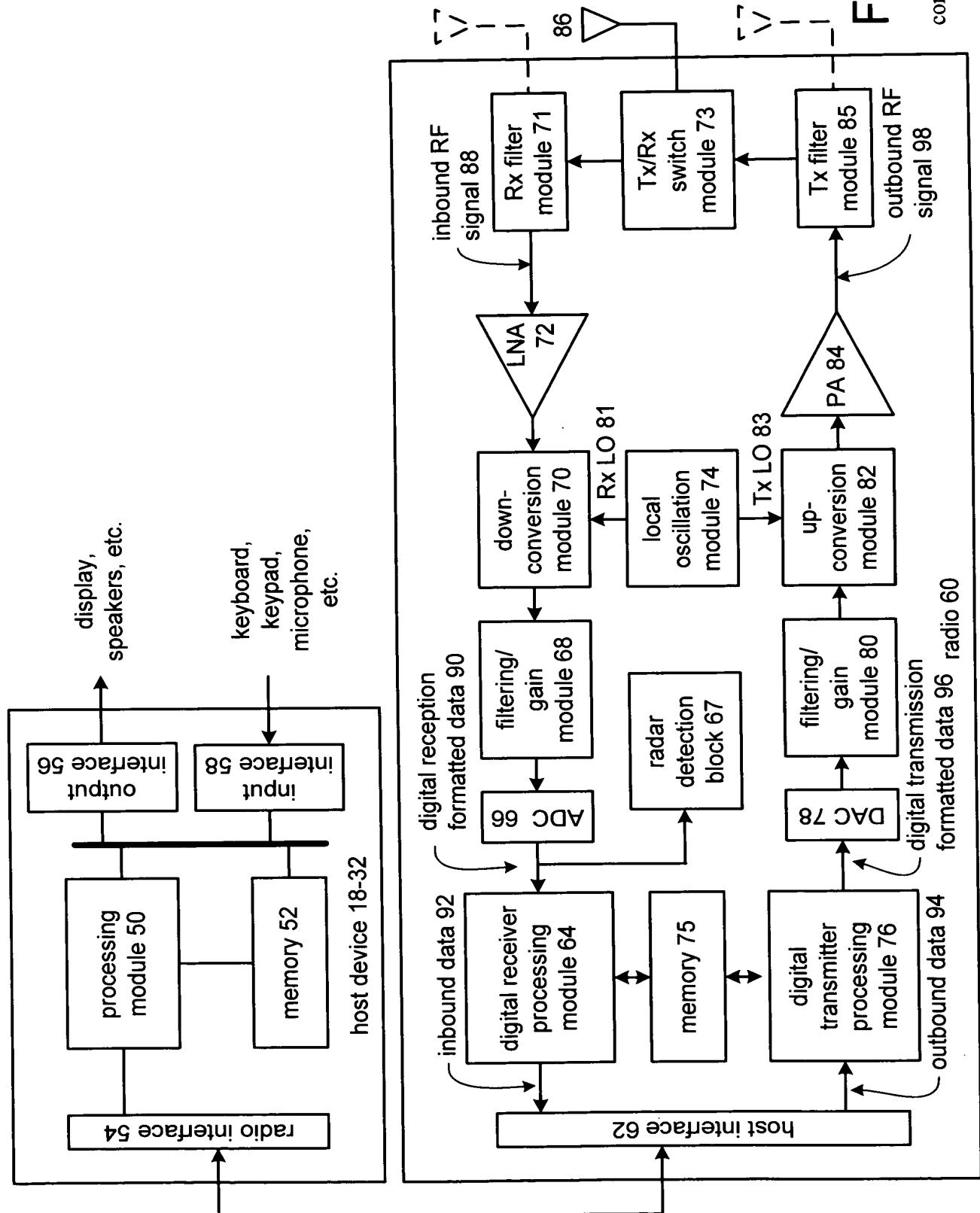


FIG. 2

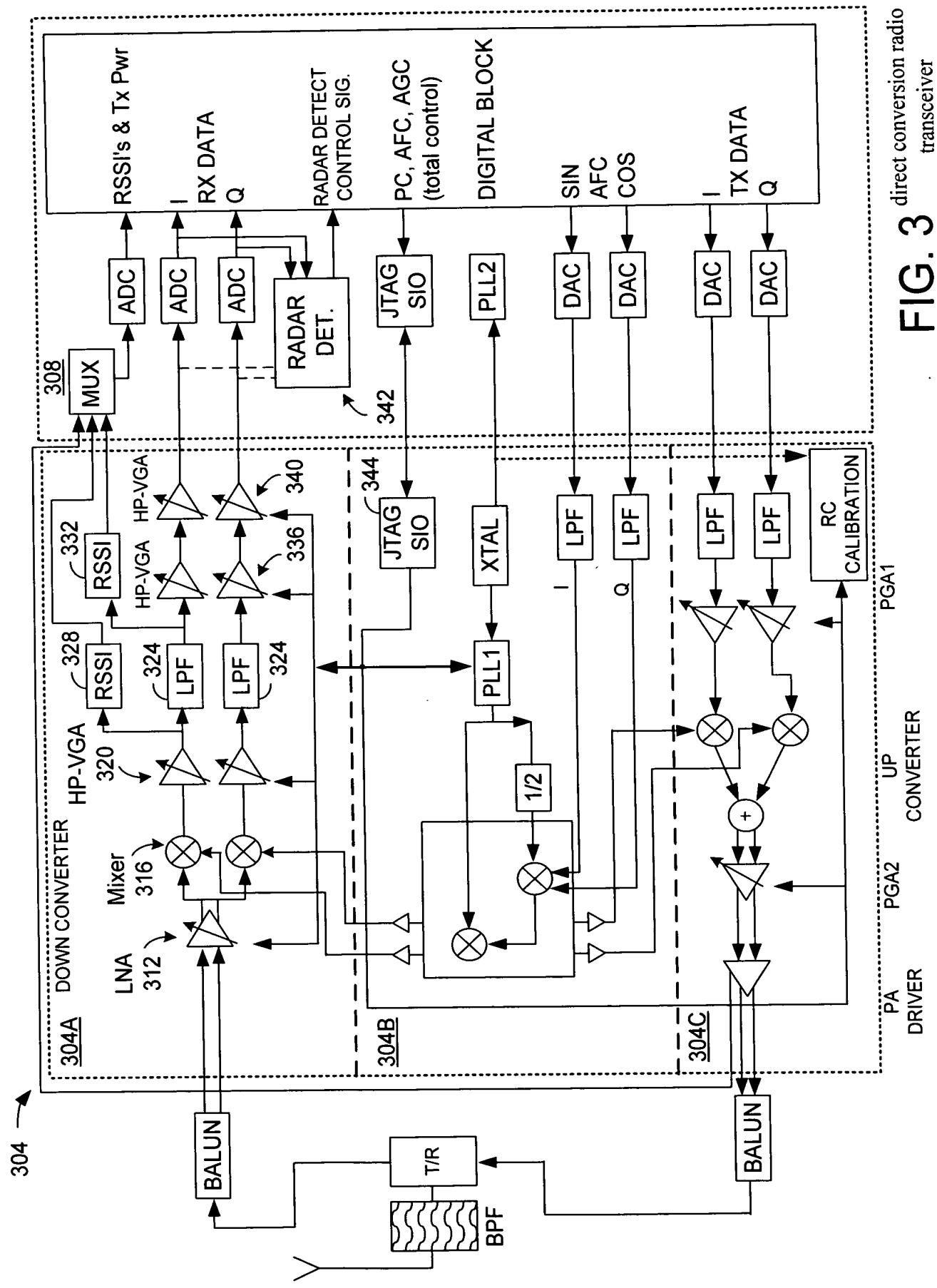


FIG. 3 direct conversion radio transceiver

FIG. 3

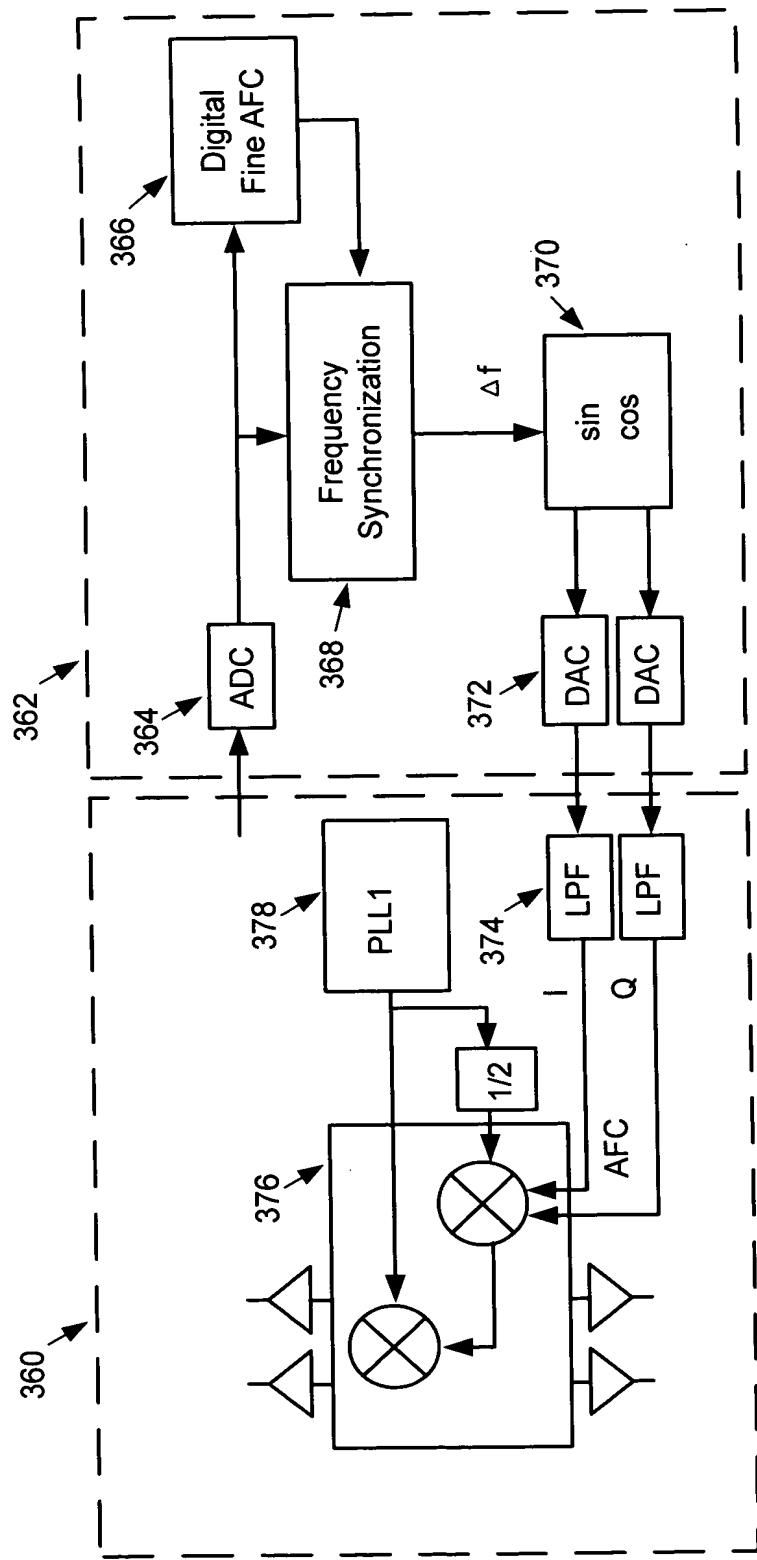


FIG. 4 receiver automatic frequency control circuit

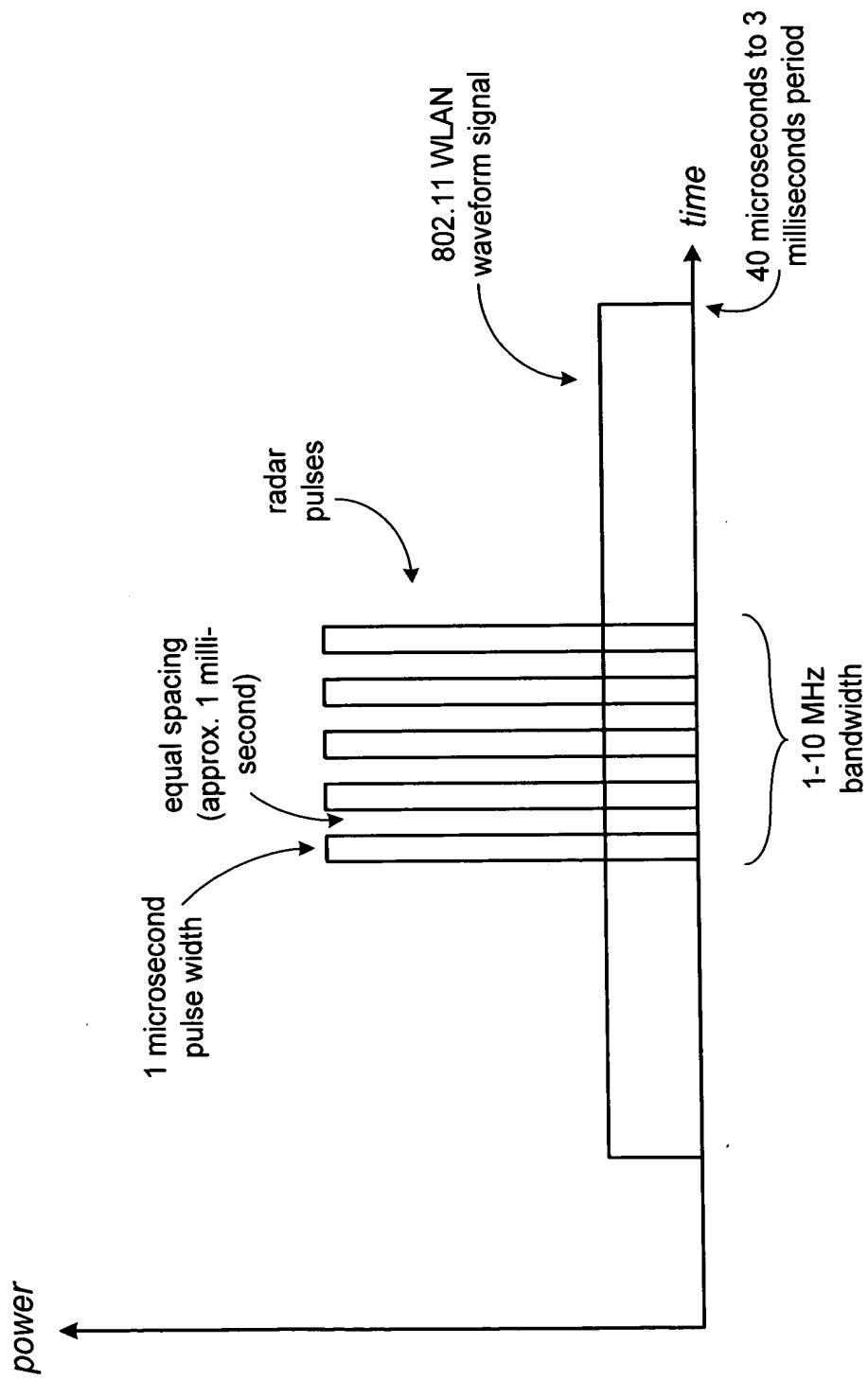


FIG. 5 radar and WLAN signals

6/15

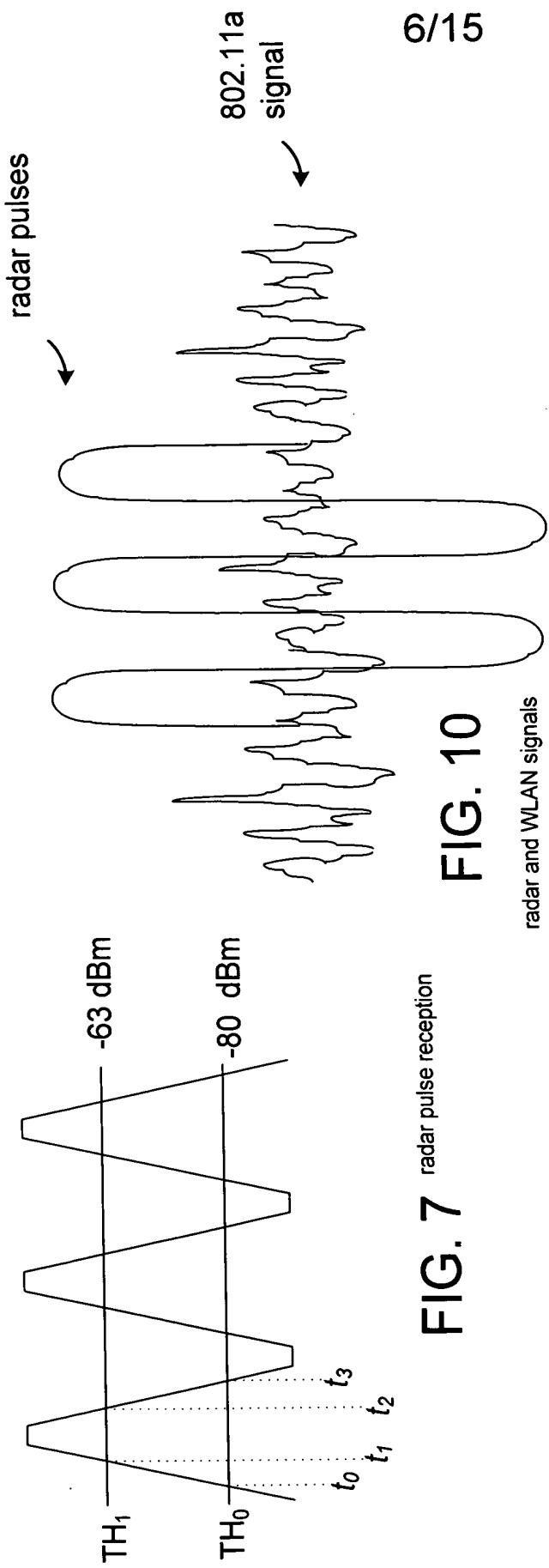


FIG. 10

radar and WLAN signals

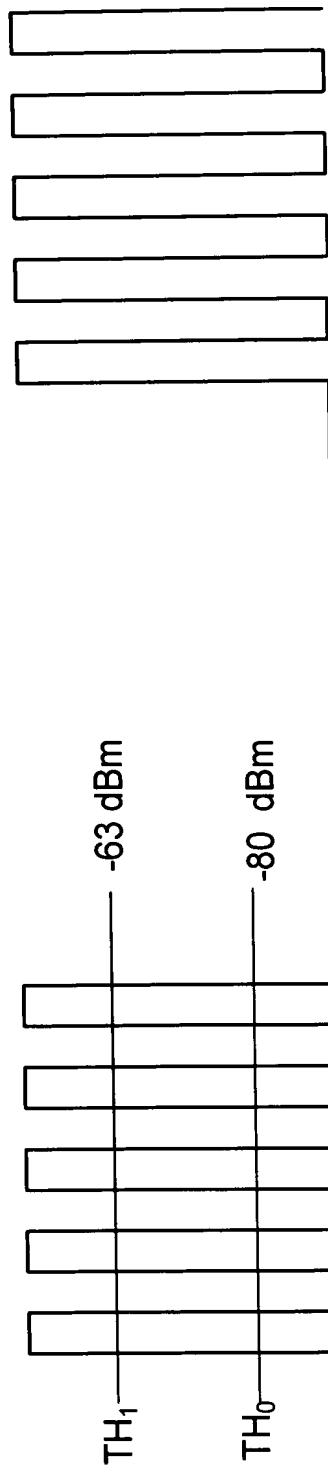


FIG. 6 groups of radar pulses

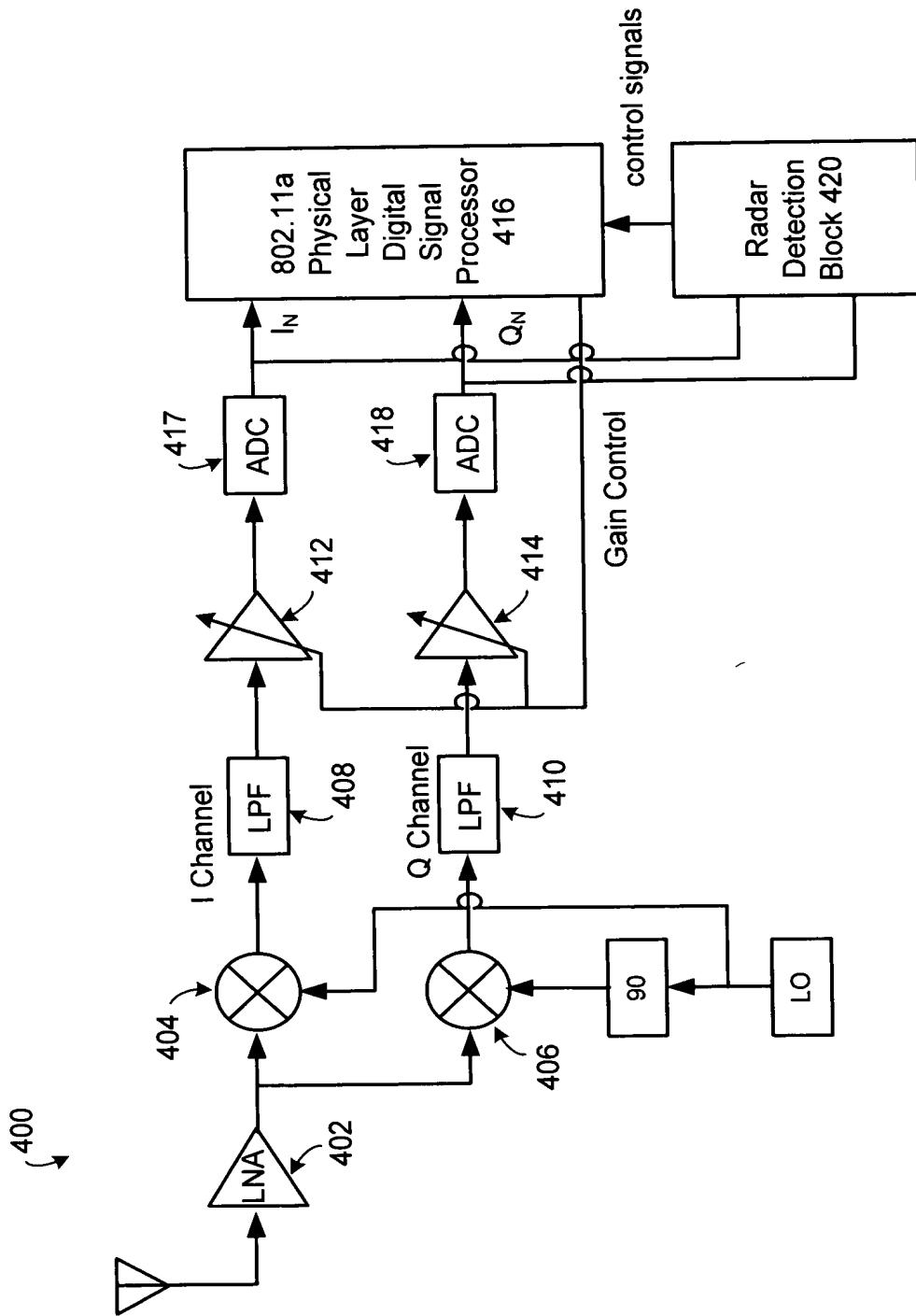


FIG. 8 receiver with radar detection block

To Processor

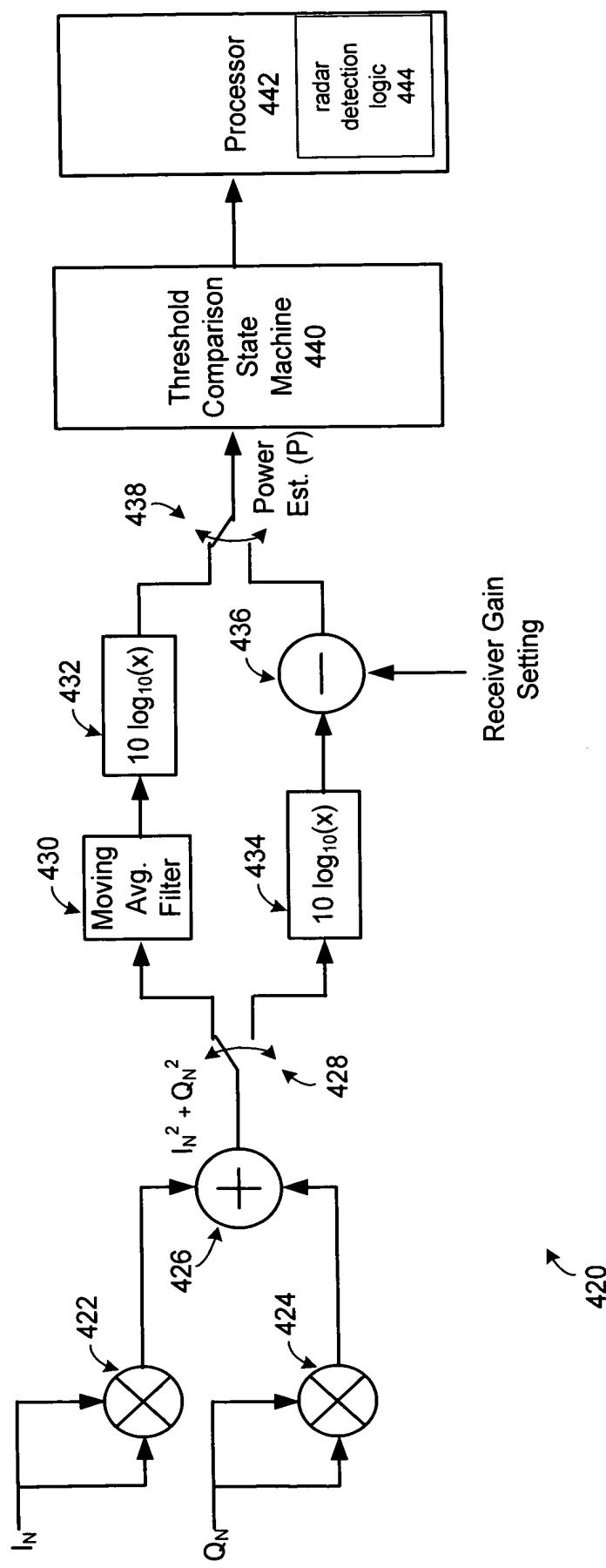


FIG. 9
radar detection logic

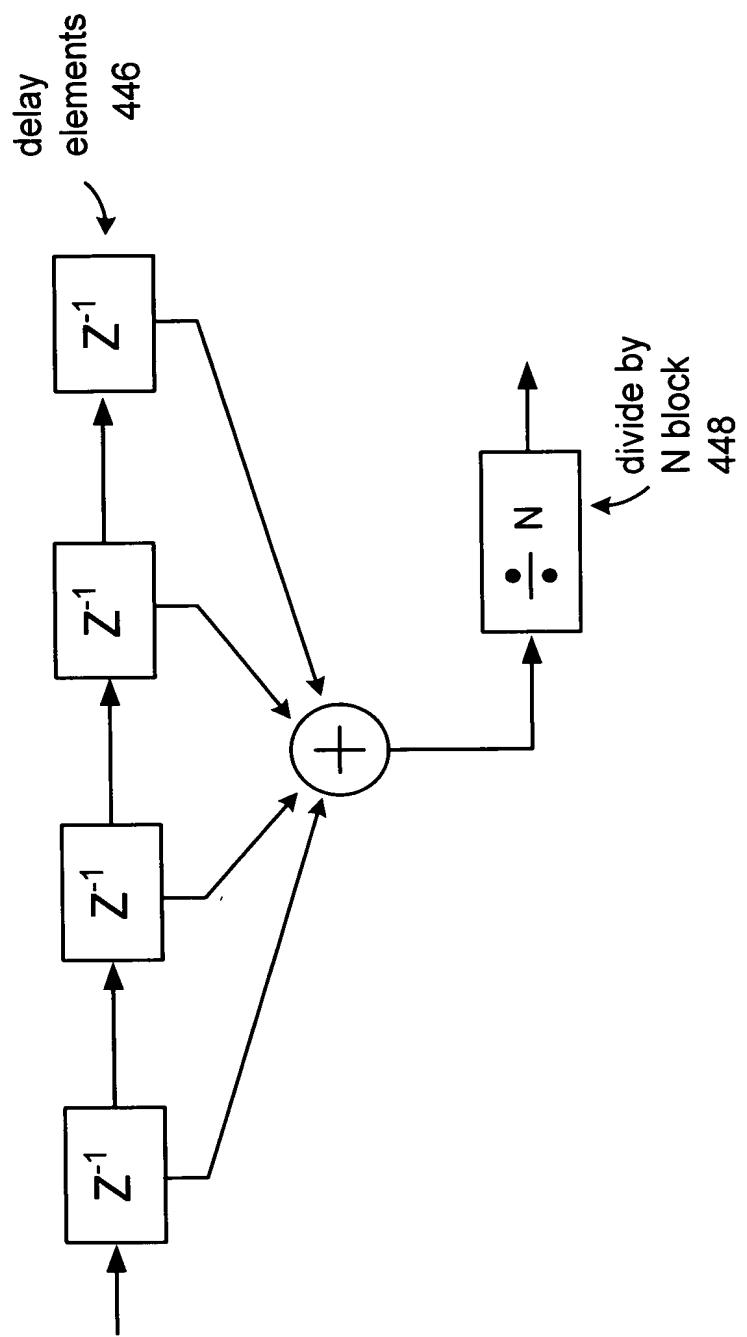


FIG. 11 moving average determination logic

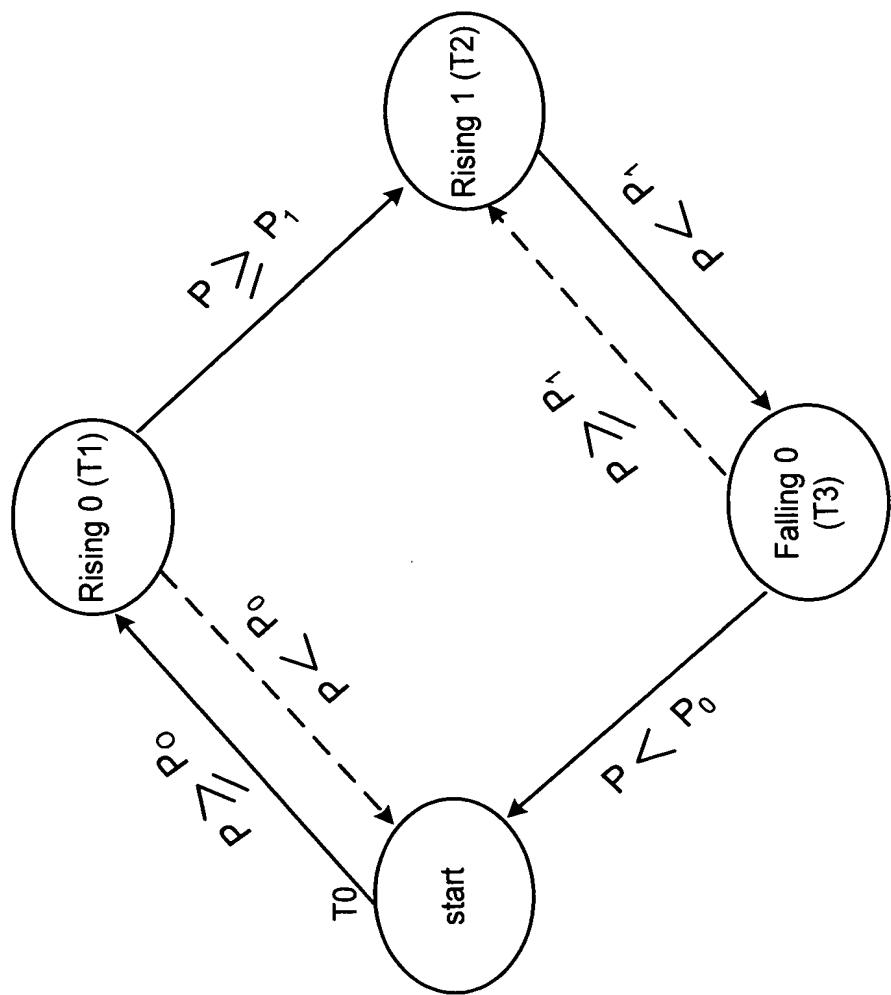


FIG. 12 state machine for detecting radar pulses

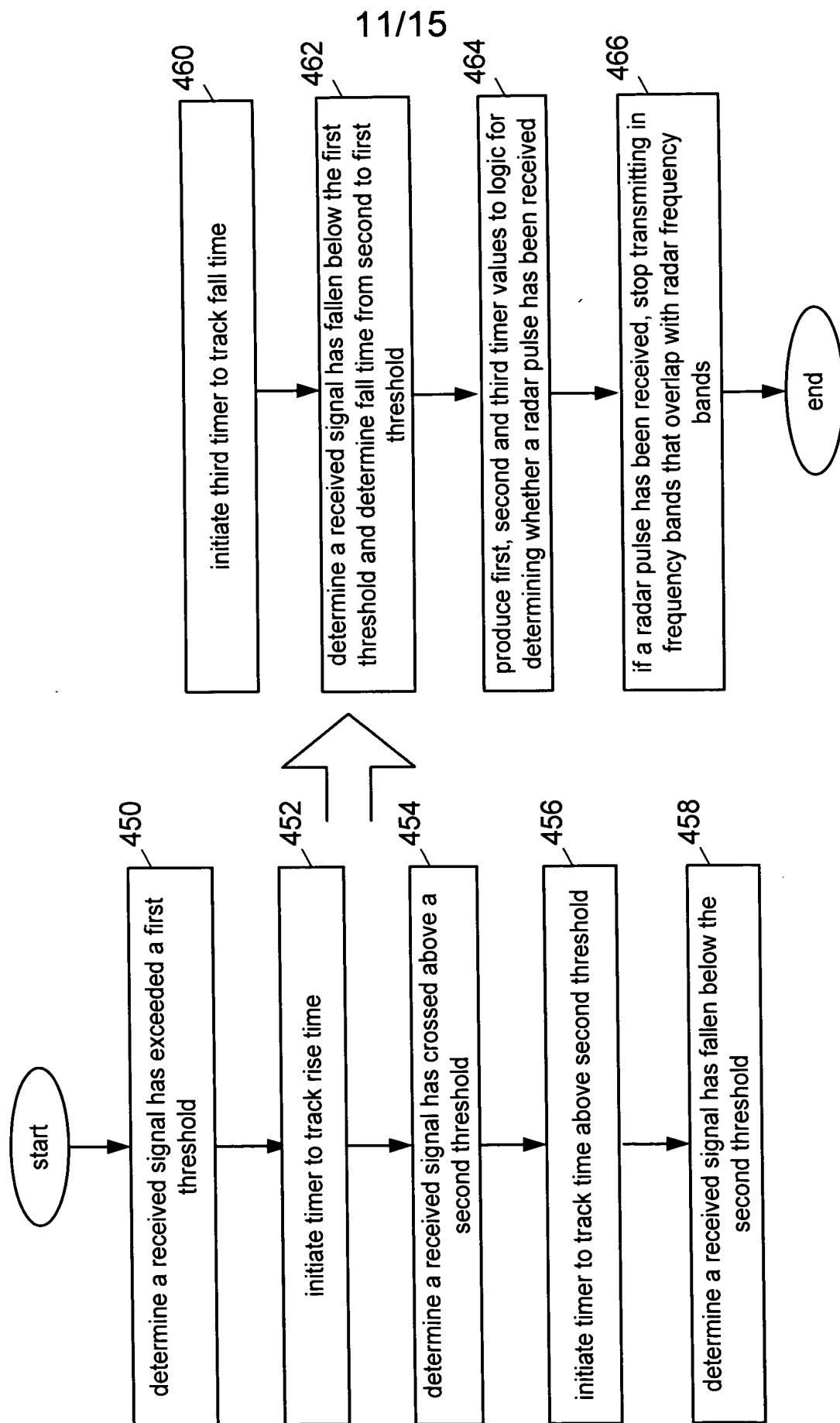


FIG. 13 method for determining if a radar pulse has been received

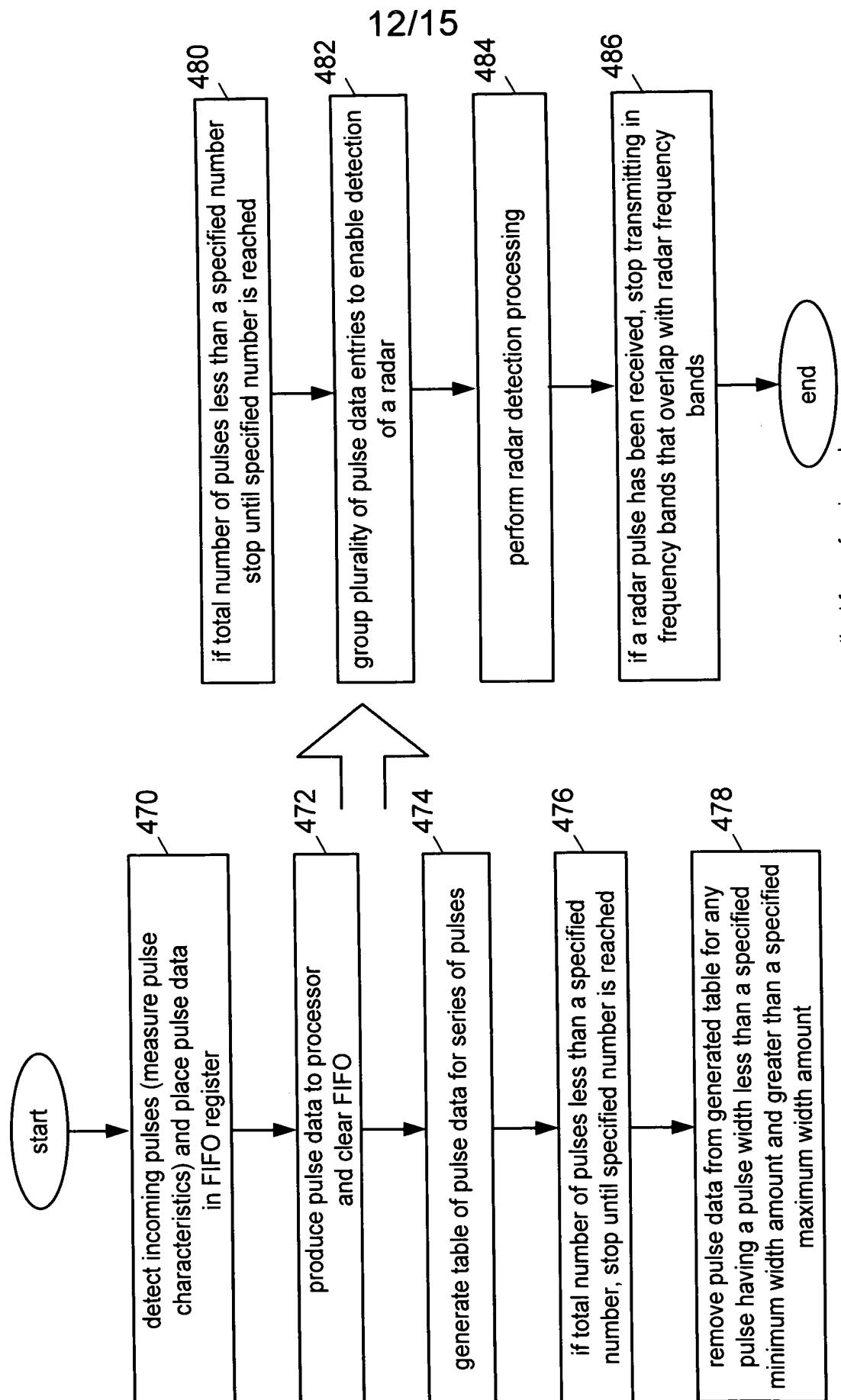
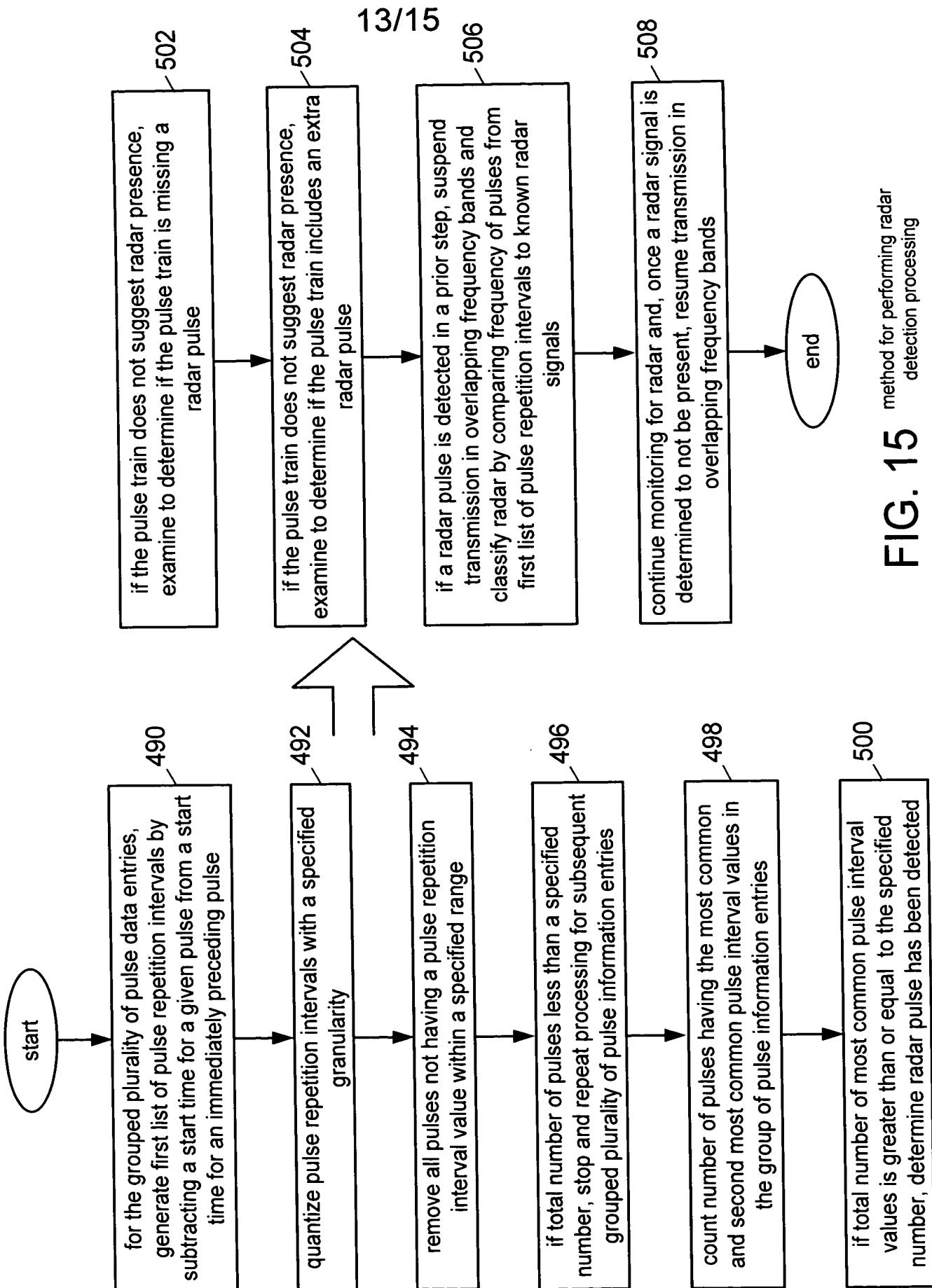


FIG. 14 method for performing radar detection processing



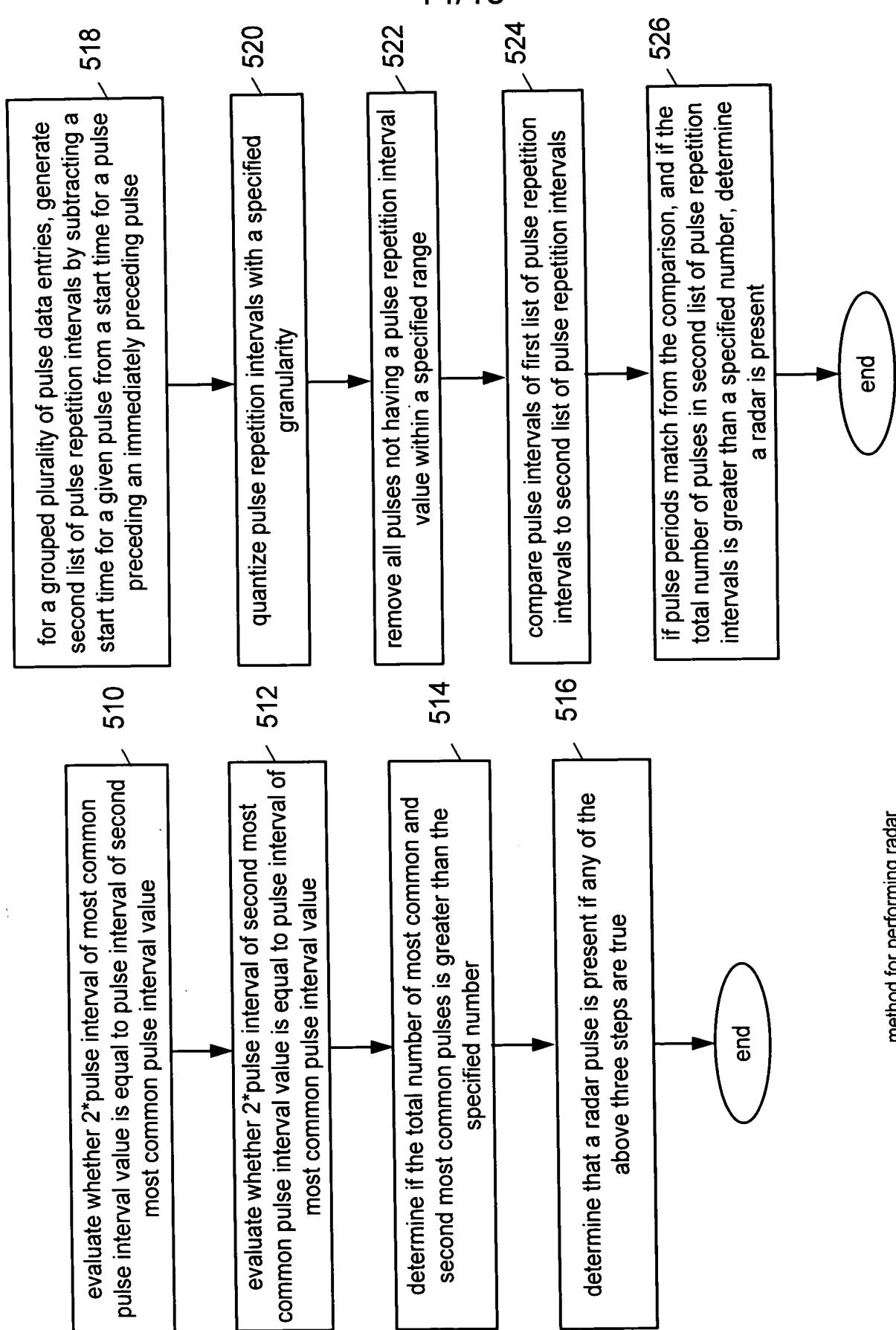


FIG. 16 method for performing radar detection processing for missing pulses

EIG 17 method for performing radar detection processing for extra pulses

start time	rise time	pulse width	fall time
A	B	C	D
E	F	G	H
I	J	K	L
M	N	O	P
Q	R	S	U
W	X	Y	Z

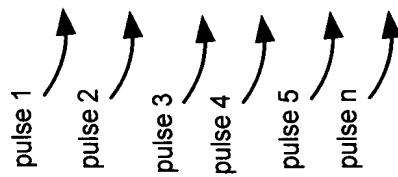


FIG. 18

method for performing radar detection processing

pulse interval	pulse interval quantity by type
E - A	n1 E - A
I - E	n2 I - E
M - I	n3 M - I
Q - M	n4 Q - M
W - Q	n5 W - Q

FIG. 19 method for performing radar detection processing for missing pulses

pulse interval	pulse interval quantity by type
I - A	n1 I - A
M - E	n2 M - E
Q - I	n3 Q - I
W - M	n4 W - M

FIG. 20 method for performing radar detection processing for extra pulses